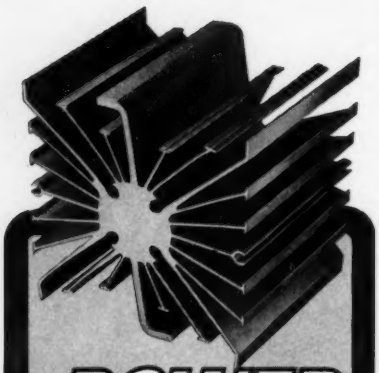

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index by subject

Circuit Design

Designing High-Power Varactor Multipliers (Jan 80) **Ftr**
Designing FET Oscillators (Jan 86) **Ftr**
How to Zap a Zener (Feb 76) **Ftr**
Pulse-Amplifier Distortion in Bandpass Filters (Feb 81) **Ftr**
Calculating Amplifier Poles and Zeros With a Digital Computer (Feb 94)) **Ftr**
High-Pass RL-Filter Nomogram (Feb 103) **Ftr**
Gain-Error Nomograms for Op Amps (Feb 104) **Ftr**
Down, But Not Out (Mar 74) **Pr**
Biasing FET Amplifiers (Mar 78) **Ftr**
Tom Thiele Challenges SCRs for Motor Control (Mar 88) **Ftr**
Wide-Range Triangular-Wave Generator (Mar 96) **Ftr**
Ten Commandments for the Care and Handling of Precision Pots (Mar 100) **Ftr**
Which Front End for Low-Noise Receivers? (Apr 22) **Pr**
Suppressing Harmonics With Inductive Coupling (Apr 76) **Ftr**
Failsafe Circuits (Apr 94) **Ftr**
Zero-Voltage Switching for Proportional Temperature Control (Apr 97) **Ftr**
How to Convert Between Small-Signal Parameters (Apr 100) **Ftr**
Charting the Program Jungle (May 28) **Ftr**
Reverse Monte Carlo for Computer-Aided Circuit Analysis (Jun 68) **Ftr**
Designing Wideband Crystal Filters With Cauer Parameters (Jun 88) **Ftr**
Jim Kendler of IBM Speaks Out On Computer-Aided Design (Jun 92) **Ftr**
Monolithic Op Amp With Simplified Frequency Compensation (Jul 58) **Ftr**
Solving Noise Problems in Digital Systems (Sep 79) **Ftr**
Designing Tunable Notch Filters With Distributed RC Networks (Sep 90) **Ftr**
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A-Greater-Than-B Logic Circuits (Nov 88) **Ftr**
Designing Reversible Binary-to-BCD Converters (Dec 58) **Ftr**

Circuit Packaging

New Op Amps Ignore Power-Supply Swings (Jan 22) **Pr**
The Significant Differences in Products at IEEE '67 (Mar 23) **SG**
New Logic Family Combines Old Ones (Mar 68) **Pr**
Concentric Trimmers Save Panel Space (Apr 141) **CPF**
Power Diff Amp Uses Single Supply (Jun 26) **Pr**
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Focused Infrared Solders Flat Cable (Jun 37) **Pr**
Multiconductor Cable Connectors (Jun 76) **SG**
EEE Significant Differences at WESCON (Aug 46) **SG**
Tough Times for Mil-C-26500 Connectors (Oct 42) **Pr**
Flat Cable Holes (Nov 41) **Pr**
New LSI Packages (Nov 44) **Pr**
Hinge/Connector for Double-Sided PC Access (Nov 131) **CPF**
Trend to Functional Modules Built With ICs (Dec 28) **Pr**
Panel Hardware and Panel-Mounted Components (Dec 46) **SG**

Circuits

The Man Behind the Classic: Henry J. Round (Jan 56) **CC**
Threshold Detector Uses IC (Jan 114) **CDA**
Readout Circuit for Digital ICs (Jan 115) **CDA**
Differentiating Amplifier Intensifies Scope Trace (Jan 115) **CDA**
One-Shot Has Improved Temperature Stability (Jan 116) **CDA**
Improved Astable Multivibrator (Jan 116) **CDA**
Free-Running Ring Counter Uses Relays (Feb 144) **CDA**
Pulse Generator With Variable Rate and Width (Feb 145) **CDA**
Pulse-Width Sorter (Feb 146) **CDA**
Frequency Doubler for Relaxation Oscillators (Feb 147) **CDA**
Clamp Circuit for Operational Integrator (Feb 148) **CDA**
Second Breakdown Gives Fast Pulses (Mar 162) **CDA**
Modified Emitter Follower Has No Offset (Mar 164) **CDA**
Peak-Hold Circuit (Mar 164) **CDA**
Three-State Indicator (Mar 166) **CDA**
Variable-Voltage Current Sink (Mar 166) **CDA**
The Man Behind the Classic: Reginald A. Fessenden (Apr 67) **CC**
Boxcar Circuit Uses FETs (Apr 136) **CDA**
Modified Limiter With Improved Accuracy (Apr 138) **CDA**
Buffered Detector (Apr 138) **CDA**
Photocell Threshold Circuit (Apr 139) **CDA**
Amplitude-Stabilized Oscillator (Apr 139) **CDA**
The Crystal Oscillator (May 178) **CC**
Fault Alarm Circuit (May 180) **CDA**
Low-Noise Preamplifier Uses FET (May 182) **CDA**
Wide Range VCO Uses IC (May 182) **CDA**
Two-Pulse Monostable (May 184) **CDA**
Solid-State Relay (Jun 136) **CDA**
Passive Dc Converter for Geiger Counter (Jun 137) **CDA**
One-Shot Multivibrator Kills Switch Bounce (Jun 137) **CDA**
Multi-Shot Trigger Circuit (Jun 138) **CDA**
Frequency Comparator Uses ICs (Jul

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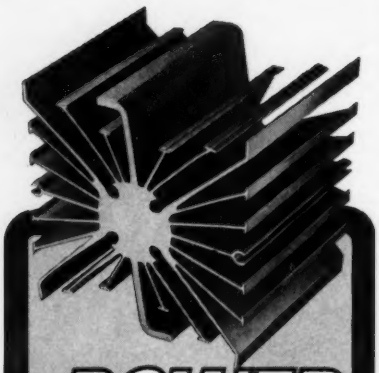

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TET = Test Equipment Trends

128) CDA
Radiation Meter Uses MOSFET (Jul 129) CDA
High-Speed Pulse Transmission Gate (Jul 130) CDA
Square-Wave Generator Uses IC Flip-Flop (Jul 131) CDA
Combined Tachometer and Dwell Meter (Aug 126) CDA
Double-Balanced Diode Mixer (Aug 127) CDA
Zenerless Regulator With Adjustable Tempco (Aug 128) CDA
FET Improves Line Regulation (Aug 130) CDA
Fail-Safe Temperature Sensor (Sep 132) CDA
PIN-Diode Turnoff for Microwave VCO (Sep 133) CDA
Pulse Integrator Gives Constant Slopes (Sep 134) CDA
Complementary Series Multivibrator (Sep 135) CDA
Simplified One-Shot Multivibrator (Oct 140) CDA
Improved One-Shot With Fast Recovery (Oct 142) CDA
Low-Cost Manual Pulser (Oct 143) CDA
Sample-and-Hold Circuit is Fast Yet Accurate (Oct 144) CDA
Inexpensive Video Distribution Amplifier Converts Logic Signals for TV Displays (Nov 124) CDA
Switching Circuit Ensures Full First Pulse (Nov 126) CDA
SCRs Simplify Monostable Coil-Driver (Nov 129) CDA
IC Op Amp Improves Stability of Monostable Multivibrator (Dec 92) CDA
Unijunction-Transistor Oscillator Locks to Line Frequency (Dec 93) CDA
MOSFET Boosts Output Impedance of Simple Current Regulator (Dec 94) CDA
Simple Bi-Quinary to BCD Converter (Dec 95) CDA

Components

New Op Amps Ignore Power-Supply Swings (Jan 22) Pr
Low-Force Battle in Snap Actions (Jan 24) Pr
Semiconductor Frequency Race Continues in Earnest (Jan 26) Pr
Light-Emitting Diodes: Something Old — Something New (Jan 30) Pr
Additives for Solar Cells and Light Emitters (Jan 30) Pr
New Semiconductor Specs (Jan 94) SD
Compromise Photochopper Cells Simplify Decision Making (Feb 30) Pr
Unijunction Transistors (Feb 70) SG
Bill Shockley of Collins Radio Switches from Carried to Bare Chips (Feb 88) Ftr
New Semiconductor Specs (Feb 112) SD
The Significant Differences in Products at IEEE '67 (Mar 23) SG
Newest Solid-State Phenomenon: The LSA Diode (Mar 64) Pr
The Microphone (Mar 77) CC
Tom Thiele Challenges SCRs for Motor Control (Mar 88) Ftr
Ten Commandments for the Care and Handling of Precision Pots (Mar 100) Ftr
New Semiconductor Specs (Mar 128) SD
Panel Meters (Apr 70) SG
New Semiconductor Specs (Apr 106) SD
Better Relays, Circuits: Conference Theme (Jun 22) Pr
New Semiconductor Specs (Jun 104) SD
Fairchild Seeks Piece of Rf Power Action (Jul 32) Pr
The Carbon-Composition Resistor (Jul 56) CC
EEE Passive Components Reference: RCL (Jul 67) SG
Carl Turner of RCA Explores Selection of Second-Breakdown-Resistant Transistors (Jul 82) Ftr
New Semiconductor Specs (Jul 100) SD
A Compact Joystick Pot (Aug 24) Pr
FETs (Aug 34) SG
EEE Significant Differences at WESCON (Aug 46) SG
Newest Ceramic Carrier is Round Version of LID (Sep 24) Pr
Powerless Pulse Generator (Sep 30) Pr
New Semiconductor Specs (Sep 100) SD
A Very Fast, Very Sensitive Reed-Like Relay (Oct 32) Pr
Tough Times for Mil-C-26500 Connectors (Oct 42) Pr
Newest Solid-State Device is Gunn-Effect FET (Oct 44) Pr
FETs as Constant Current Sources (Oct 82) Ftr
Solid-State Unit Replaces Ignitron (Nov 22) Pr
Rf Power Transistor Provides 50 Watts at 500 MHz (Nov 26) Pr
New Solid-State Challenger (Nov 28) Pr
Panel Hardware and Panel-Mounted Components (Dec 46) SG

Computers

New Design One-Ups Logic Experts (Feb 37) Pr
LSI and TF Memories in Tomorrow's Computers (Apr 36) Pr
Charting the Program Jungle (May 28) Pr
Software for Sale (Dec 28) Pr

Design Aids

High-Pass RL-Filter Nomogram (Feb 103) Ftr
Gain-Error Nomograms for Op Amps (Feb 104) Ftr
Scales Simplify Graph Reading (Mar 94) Ftr
Bandwidth Nomogram for Best S/N Ratio in TV-Camera Systems (Apr 84) Ftr
Doppler Frequency Slide Rule (Sep 70) Ftr
Integrated-Circuit Reference Nomogram (Dec 42) Ftr

Lasers

Data Processing is Laser Bright Spot (Feb 30) Pr
Are Holograms Headed for Hollywood? (Mar 72) Pr
TV Audience Sees Red (Apr 35) Pr
Looking at Fast Laser Pulses (Jun 36) Pr
A Hologram is Worth a Thousand Words (Jun 42) Pr
The Bright Light Moves On (Oct 18) Pr

Materials

Hot Wires Not So Hot (Mar 72) Pr
New Mag Tape for Denser Recording (Sep 28) Pr

Measurement Techniques

How to Select the Right Probe (Jan 42) MF
How to Measure Common-Mode Effects (Feb 48) MF
How to Detect Excessive Time-Interval Ratios by Analog Methods (Mar 108) MF
Tracking Down Second Closure in Mercury Relays (Apr 44) MF
How to Measure and Specify Parasitic Noise Current (Jun 48) MF
The Effects of Superimposed Noise on DVM Measurement (Sep 34) MF
How to Use Programmed Measurements (Oct 48) MF

Memories

Candlemaking and Weaving for Better Memories (Feb 38) Pr

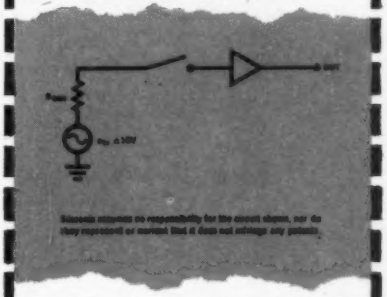
Microelectronics

Tom Hyltin of TI and his Microwave ICs (Jan 74) Ftr
New Semiconductor Specs (Jan 94) SD
ICs Improve Performance of Hi-Fi Stereo Receivers (Feb 26) Pr
Fast Tacks for Flat Packs (Feb 43) Pr
New Semiconductor Specs (Feb 112) SD
Program Set for Linear IC Clinic (Mar 42) Pr
Tantalum's Popularity Grows in Thick-Thin-Film Controversy (Mar 44) Pr
Chromium-Silicon Dioxide Thin Films Pass IBM Tests (Mar 61) Pr
New Logic Family Combines Old Ones (Mar 68) Pr

Application For

FET SWITCHES

PROBLEM: Sample and Hold Circuit

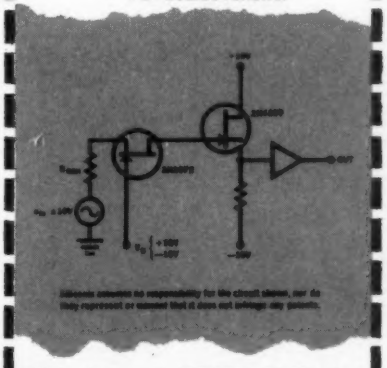


REQUIRED - Circuit that will sample e_{in} with a 200 ns aperture . . . and hold sample 0.01 sec. minimum.

GIVEN

e_{in} ±10V . . .
Available supply voltages ±15V . . .
R_{GEN} 25 ohms . . .

SOLUTION - FET switch and FET source follower



FET switch used - 2N3972 with V_P less than 3V and r_{ds} below 100 ohms.

Gate drive to turn ON . . . +10V
to turn OFF . . . −15V

(2N4339 used as source follower for high input impedance.)

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Interest Rises in LSI (Mar 66) Pr
New Semiconductor Specs (Mar 128) SD
Linear ICs Making Gradual Inroads (Apr 36) Pr
New Semiconductor Specs (Apr 106) SD
Integrated Circuits (May 56) SG
What's New in Linear ICs (May 58) SG
What's New In Digital ICs (May 61) SG
Digital IC Directory (May 71) SG
Interchangeability Guides (May 145) SG
New Semiconductor Specs (Jun 104) SD
Linear IC Replaces Op Amp Module (Jul 22) Pr
Monolithic Op Amp With Simplified Frequency Compensation (Jul 58) Ftr
New Semiconductor Specs (Jul 100) SD
Complementary MOS ICs for Sale (Aug 26) Pr
A Simplified Procedure for Reliability Evaluation of MOS LSI Circuits (Aug 86) Ftr
Hybrid IC Technology: A Semiconductor Device Manufacturer's Viewpoint (Sept 74) Ftr
Philco-Ford's Phil Ferguson Speaks Out On MOS LSI (Sep 84) Ftr
New Semiconductor Specs (Sep 100) SD
Designing Tuned Amplifiers With Integrated Circuits (Oct 60) Ftr
Power Supplies for ICs (Oct 66) SG
Murray Siegel of Fairchild Speaks Out on IC User Beefs (Oct 86) Ftr
Linear Integrated Circuits (Nov 62) SG
ICs Affect World of Long Hairs (Dec 25) Pr
Trend to Functional Modules Built With ICs (Dec 28) Pr
Wally Raisanen of Motorola Speaks Out On The "Real" World of ICs (Dec 38) Ftr
Integrated-Circuit Reference Nomogram (Dec 42) Ftr

Miscellaneous

New Firm Improves Any Product Specs (Jun 40) Pr
The Contributions of Edsel Murphy to the Understanding of the Behavior of Inanimate Objects (Aug 91) Ftr
No FishCall (Sep 30) Pr

Power Sources

Fuel Cells Take a Deep Breath (Feb 43) Pr
Ac Battery? (Apr 40) Pr
High Power in Small Packages (Jul 27) Pr
Power Supplies for ICs (Oct 66) SG
Computer Interface for Programmable Supplies (Dec 22) Pr

Test Equipment

A Survey of Lab-Type Integrated Testers (Jan 48) TET
Graphic Recorders (Jan 62) SG
A Survey of Volt-Ohm-Milliammeters (Feb 54) TET
The Heising Modulator (Feb 69) TEC
The Significant Differences in Products at IEEE '67 (Mar 23) SG
Wide-Range Triangular-Wave Generator (Mar 96) Ftr
A Survey of Frequency Synthesizers (Mar 114) TET
Low-Cost DVOM Bids for Analog-Meter Market (Apr 30) Pr
A Survey of Noise Generators (Apr 50) TET
Panel Meters (Apr 70) SG
Loebe Julie of Julie Research Speaks Out On What's Wrong With Precision-Dc-Instrument Specs (Apr 86) Ftr
Two Scopes Race Ahead in Speed, Sensitivity (May 22) Pr
Analog Recorder Prints Digital Data, Too (May 24) Pr
Metrology for Everyman (May 35) Pr
A Survey of Oscilloscope Cameras (May 40) TET
Myles Judd of Hewlett-Packard Speaks Out On What's Wrong With Sweeper Specs (May 46) Ftr
More DVMs Aim for Low-Cost Market (Jun 24) Pr
X-Ray Snapshooter Makes Handy Lab Tool (Jun 28) Pr
A Survey of Electrometers (Jun 44) TET
The Field Strength Meter (Jun 67) TEC
Kits Compete for Quality Market (Jul 35) Pr
A Survey of Sweep Generators (Jul 38) TET
A Survey of Analog Frequency Meters (Aug 20) TET
Multirange Electrometers: Tube vs MOSFET (Aug 26) Pr
EEE Significant Differences at WESCON (Aug 46) SG
Lou Costrell of NBS Speaks Out On The Revolution in Instrument Packaging (Aug 78) Ftr
Meterless Differential Voltmeter Obviates Operator Judgment (Sep 26) Pr
More Digital Competition for d'Arsonval (Sep 32) Pr
A Survey of Lab-Type Semiconductor Testers (Sep 42) TET
Impedance Bridges and Meters (Sep 56) SG
Simplified Operational Amplifier Tester (Sep 66) Ftr
The British Are Coming (Oct 30) Pr
Leapfrog in Fast Counters (Oct 33) Pr
A Survey of Pulse Generators (Oct 50) TET
Two Wideband Test Probes (Nov 20) Pr
Another Vote for Plug-In Semi Testers (Nov 30) Pr
Two Single-Box Sweepers With Ultrawide Linear Sweep (Nov 46) Pr
A Survey of Wave Analyzers (Nov 50) TET
Jack Hubbs of E-H Research Speaks Out On What's Wrong with Pulse Instrumentation (Nov 56) Ftr
Direct/Differential Multimeter for Fast/Accurate E-I-R Measurements (Dec 20) Pr
A Survey of Q and RX Meters (Dec 30) TET

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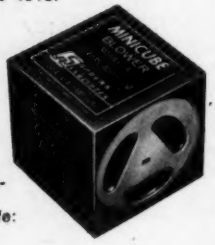

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